## LESSON PLAN

SUB:-POWER ELECTRONICS & PLC.

BRANCH:- ELECTRICAL ENGG.

**SEMESTER: 5TH** 

SESSION:2022-2023

NAME OF FACULTY: - ASHWINI KUMAR SAHU



## GOVERNMENT POLYTECHNIC, BHADRAK

HOD (FLECT.) G.P.BHADRAK Academic 6-optimator

Principal
Govt. Polytethine Bhadrak
Govt. Polytechnic
Bhadrak

Discipline: ELECTRICAL ENGG.	Semester: 5 <sup>th</sup>	Name of the Teaching Faculty : ASHWINI KUMAR SAHU Sr.Lect in Elect.Engg.
Subject: POWER ELECTRONICS AND PLC	No. of Days/per week class allotted:4	Semester from date: 15.09.2022 To Date: 21.01.2023  No. of Weeks:15
Week	Class Day	Theory
1st	1st	Construction, Operation, V-I characteristics & application of power Diode.
<b>不是</b>	2 <sup>nd</sup>	Construction, Operation, V-I characteristics & application of SCR
	3rd	Construction, Operation, V-I characteristics & application of DIAC & TRIAC
	4th	Construction, Operation, V-I characteristics & application of Power MOSFET
2 <sup>nd</sup>	1st	Construction, Operation, V-I characteristics & application of GTO & IGBT
	2 <sup>nd</sup>	Two transistor analogy of SCR
	3rd	Gate characteristics of SCR.
A STATE OF THE STA	4 <sup>th</sup>	Switching characteristic of SCR during turn on and turn off.
3rd	1st	Turn on methods of SCR
• 0	2 <sup>nd</sup>	Turn off methods of SCR (Line commutation and Forced commutation)
	3rd	Load Commutation Resonant pulse commutation
	4th	Voltage and Current ratings of SCR
4th	1st	Protection of SCR Over voltage protection
	2 <sup>nd</sup>	Over current protection Gate protection
	3rd	Firing Circuits and General layout diagram of firing circuit 1.
	4th	R firing circuits and R-C firing circuit.
5 <sup>th</sup>	1st	UJT pulse trigger circuit and Synchronous triggering (Ramp Triggering.
	2 <sup>nd</sup>	Design of Snubber Circuits and chapter revision
	3rd	Controlled rectifiers Techniques(Phase Angle, Extinction Angle control),
	4th	Single quadrant semi converter, two quadrant full converter and dual Converter.
6 <sup>th</sup>	1 st	Working of single-phase half wave controlled converter with Resistive
	2 <sup>nd</sup>	Working of single-phase half wave controlled converter with R-L loads and Understand need of freewheeling diode.

	3rd	Working of three-phase half wave controlled converter with Resistive load
	4th	Working of three-phase fully wave controlled converter with Resistive load
7 <sup>th</sup>	1 st	Working of single phase AC regulator
	2nd	Working principle of step up chopper
,	3rd	Working principle of step down chopper
	4 <sup>th</sup>	Control modes of chopper
8th	1st	Operation of chopper in all four quadrants
	2nd	Class test of ch-1 and ch-2
	3rd	Classify inverters
	<b>4</b> <sup>th</sup>	Explain the working of series inverter
9th	1 st	Explain the working of parallel inverter
	2nd	Explain the working of single-phase bridge inverter
	3rd	Explain the basic principle of Cyclo-converter
7 .	4th	Explain the working of single-phase step up Cyclo-converter
10 <sup>th</sup>	1st	Explain the working of single-phase step down Cyclo-converter
	2 <sup>nd</sup>	
	3rd	List applications of power electronic circuits
	4th	List the factors affecting the speed of DC Motors
11 <sup>th</sup>	1st	Speed control for DC Shunt motor using converter
	2nd	Speed control for DC Shunt motor using chopper
	3rd	List the factors affecting speed of the AC Motors.
1	<b>4</b> th	Speed control of Induction Motor by using AC voltage regulator
12 <sup>th</sup>	1 <sup>st</sup>	Speed control of induction motor by using converters and inverters (V/F control)
	2 <sup>nd</sup>	Working of UPS with block diagram
	3rd	Battery charger circuit using SCR with the help of a diagram
	4th	& applications
13 <sup>th</sup>	1st	Introduction of Programmable Logic Controller(PLC) Advantages of PLC
	2nd	Different parts of PLC by drawing the Block diagram and purpose of each part of PLC
	3rd	Applications of PLC Ladder diagram
	4th	Description of contacts and coils in the following states i)Normally open ii) Normally closed iii) Energized output iv)latched Output v) branching
14th	1st	Ladder diagrams for i) AND gate ii) OR gate and iii) NOT gate.

	2nd	Ladder diagrams for combination circuits using NAND,NOR,
	211	AND, OR and NOT
n like	3rd	Timers-i)T ON ii) T OFF and iii)Retentive timer
$\frac{1}{2} = \frac{1}{2} \int_{\mathbb{R}^2} \mathbb{E} f(f) df$	4th	Counters-CTU, CTD
15 <sup>th</sup>	1st	Ladder diagrams using Timers and counters
		And PLC Instruction set
	2nd	Ladder diagrams for following (i) DOL starter and STAR-DELTA
- 5 471.1		starter (ii) Stair case lighting (iii) Traffic light Control (iv)
	, to 17	Temperature Controller
1 A	3rd	Special control systems- Basics DCS & SCADA systems
		Computer Control-Data Acquisition,
1 31 1	1	Direct Digital Control System (Basics only)
Y V	4th	Previous year question discussions

SIGNATURE OF FACULTY