LESSON PLAN

SUB: COMPUTER SYSTEM ARCHITECTURE, (TH-1)
BRANCH:- COMPUTER SCIENCE & ENGINEERING
SEMESTER:3RD

NAME OF FACULTY: LAXMIDHAR SETHY(SR.LECT.IN CSE)

GOVERNMENT POLYTECHNIC, BHADRAK

SESSION :- 2024-25

HOD ,CSE

Academic Co-ordinator

Principal 29.06.24
Govt. Polytechnic, Bhadrak
Principal
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Discipline: Computer Science & Engineering	**Semester:**3rd,W/2024	**Name of the faculty:**Laxmidhar Sethy **Email:**ldsathy@gmail.com
Subject: Computer System Architecture, (Th-1)	**No. of Days/week:**04	**StartDate:**01/07/2024 **EndDate:**08/11/2024

Week	Class Day	Theory Topics
1st	1st	**1.IntroductiontoBasicstructureofcomputerhardware**
	2nd	Introduction of computer
	3rd	Basic Structure of computer
	4th	Computer hardware
2nd	1st	Functional Units
	2nd	Computer components
	3rd	Performance measures
	4th	Memory addressing & Operations
3rd	1st	Revision
	2nd	**2.IntroductiontoInstructions&instructionSequencing**
	3rd	Fundamentals to instructions
	4th	Operands
4th	1st	Op Codes
	2nd	Instruction formats
	3rd	Addressing Modes
	4th	Continuing addressing modes
5th	1st	Revision
	2nd	Question answer discussion
	3rd	**3.IntroductiontoProcessorSystem**
	4th	Register Files
6th	1st	Complete instruction execution
	2nd	Hardware control
	3rd	Micro program control
	4th	Revision
7th	1st	Quiz–1
	2nd	**4.Introduction to Memory System**
	3rd	Memory characteristics
	4th	Memory hierarchy
8th	1st	RAM and ROM organization
	2nd	Continuing about RAM and ROM organization
	3rd	Interleaved Memory
	4th	Cache memory
9th	1st	Virtual memory
	2nd	Revision
	3rd	Question answer discussion
	4th	**5.IntroductiontoInput–OutputSystem**
10th	1st	Input-Output Interface

	2nd	Modes of Data transfer
	3rd	Programmed I/O Transfer
	4th	Interrupt driven I/O
11th	1st	DMA
	2nd	I/O Processor
	3rd	Continuing I/O Processor
	4th	Revision
12th	1st	Question answer discussion
	2nd	**6.Introduction to I/O Interface & Bus architecture**
	3rd	Bus and System Bus
	4th	Types of System Bus
13th	1st	Bus Structure
	2nd	Basic Parameters of Bus design
	3rd	SCSI
	4th	USB
14th	1st	Revision
	2nd	Quiz–2
	3rd	**7.Introduction to Parallel Processing**
	4th	Parallel Processing
15th	1st	Linear Pipeline
	2nd	Multiprocessor
	3rd	Flynn"s Classification
	4th	Revision

Signature of faculty