LESSON PLAN

SUB:-ANALOG ELECTRONICS LAB BRANCH:- ELECTRICAL ENGG.

SEMESTER: 4TH

NAME OF FACULTY: - TAPAN KU. DAS



GOVERNMENT POLYTECHNIC, **BHADRAK SESSION:2024-25**

HOD (10.36T.)

Academic Co-ordinator

Academic Co-ordinator

Govt. Polytechnic Bhadrak

Govt. Police schnic

DISCIPLINE ELECTRICAL ENGG.	SEMESTER 4 TH	NAME OF THE TEACHING FACULTY: TAPAN KU. DAS (Lect. in Electronics Engg.)
SUBJECT: ANALOG ELECTRONICS LAB	NO. OF DAYS/WEEK CLASS ALLOTTED – 45 (3P/week)	SEMESTER FROM DATE 04.02.2025 – 17.05.2025
WEEK	· CLASS DAY	PRACTICAL TOPICS
lst	E ₁	Determine the input and output Characteristics of CE & CB transistor configuration
	E ₂	Determine the input and output Characteristics of CE & CB transistor configuration
2nd	E ₁	Determine Drain & Transfer Characteristics of JFET
	E ₂	Determine Drain & Transfer Characteristics of JFET
PB Y	$\mathbf{E_1}$	Construct Bridge Rectifier using different filter
		circuit and to determine Ripple factor & analyze
3rd		wave form with filter & without filter.
1 1	E ₂	Construct Bridge Rectifier using different filter
į, ž		circuit and to determine Ripple factor & analyze
		wave form with filter & without filter.
	E ₁	Construct Bridge Rectifier using different filter and to determine Ripple factor.
4th	E ₂	Construct Bridge Rectifier using different filter and to determine Ripple factor.
5TH	<u> E</u> 1	Construct & test the regulator using Zener diode
	E ₂	Construct & test the regulator using Zener diode
6TH	E_1	Construct different types of biasing circuit and
		analyze the wave form
		(i) Fixed bias (ii) Emitter bias (iii) Voltage divider bias
	E ₂	Construct different types of biasing circuit and analyze the wave form
7 TH	T.	(i)Fixed bias (ii) Emitter bias (iii) Voltage divider bias Study the single stage CE amplifier & find Gain
7	$\mathbf{E_{l}}$	Study the single stage of amplifier & find dam
	E ₂	Study the single stage CE amplifier & find Gain
8 TH	E ₁	Study multi stage R-C coupled amplifier & to
	16.9	determine frequency- response & gain.
	9./9	

		E ₂	Study multi stage R-C coupled amplifier & to determine frequency- response & gain.
9 ^{тн}	E _I	Construct & Find the gain (I) Class A. Amplifier (ii) Class B. Amplifier (iii) Class C Tuned Amplifier	
		E ₂	Construct & Find the gain (I) Class A. Amplifier (ii) Class B. Amplifier (iii) Class C Tuned Amplifier
10 TH		E_{l}	Construct & test push pull amplifier & observer the
T A		E ₂	Construct & test push pull amplifier & observer the wave form
11 TH	Walter at Low	E_1	Construct & calculate the frequency of
			Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phase shift oscillator and draw wave form & calculate the frequency
		E_2	Construct & calculate the frequency of
			Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phase shift oscillator and draw wave form & calculate the frequency
12 TH	TANKS .	E_1	Construct & Test Differentiator and Integrator using R-C Circuit
<u>*</u>		E_2	Construct & Test Differentiator and Integrator using R-C Circuit
13 TH	r gran	E_1	Study Multivibrator (Astable, Bistable, Monstable) Circuit & Draw its Wave forms
		E_2	Study Multivibrator (Astable, Bistable, Monstable) Circuit & Draw its Wave forms
14 TH		$\mathbf{E_{l}}$	REVISION
1		E ₂	REVISION
15 TH	775	E_1	REVISION
148	A 200 W	E_2	REVISION

SIGNATURE OF THE FACULTY
Oct.in Elect.Engg.
Govt.Poly.Bhadrak